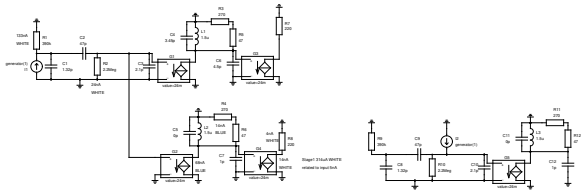

A1	Ocvcap directive	include sim/models/ideNRE_wand.lib
A1B	Ocvcap directive	include sim/models/ideNRE_best.lib
A1Y	Ocvcap directive	include sim/models/ideNRT_wand.lib
A1B	Ocvcap directive	include sim/models/ideNRT_best.lib
A2	Ocvcap directive	include sim/models/ideNYIL_wand.lib
A2	Ocvcap directive	include sim/models/ideNYIL_best.lib
A3	Ocvcap options	numdgt=8 out=10000 stabtol=ncvp
A5	Ocvcap directive	op
A6	Ocvcap directive	print ac ireal(D2) ireal(Q4) vp(D2) vp(Q4)
A7	Ocvcap directive	ac 1 1% 8.6G Dec 50 -cshr/fet/data Basic Quiet
A8	Ocvcap directive	print ac ireal(Q1 ireal(Q4) ireal(R9)
A9	Ocvcap directive	ac 100Meg -i-shr/fet20 data Basic Quiet

Coil Frontend

Size			
Rev	To be released under OPSL	Revision	20061102
Sheet	1 of 1	Created on	Check